

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L10	1	716/3,16-17.ccls. and (plesiochronous and (seagate or sea-gate or sea-of-gate or sea-of-platform or sea-platform or array) and (abstract\$2 or fabric) near resource).clm.	US-PGPUB; USPAT	OR	ON	2006/06/05 14:45
L11	1	(plesiochronous and (seagate or sea-gate or sea-of-gate or sea-of-platform or sea-platform or array) and (abstract\$2 or fabric) near resource).clm.	US-PGPUB; USPAT	OR	ON	2006/06/05 14:45

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	571	(fabric near5 resource)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/05 14:12
L2	5702	(plesiochronous or isochronous)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/05 14:37
L3	27	1 and 2	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/05 14:13
L4	20	3 and (translat\$4 or convert\$4 or transform\$7)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/05 14:14

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L14	380	(plesiochronous or isochronous) and fabric	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/05 14:52
L16	83	14 and (ASIC or specific adj3 circuit)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/05 14:52
L17	67	16 and map\$4	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/05 14:53
L18	10	17 and (translat\$4 or convert\$4 or transform\$7) same resource	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/05 14:56
L19	8	17 and (translat\$4 or convert\$4 or transform\$7) same (RTL or HDL or VHDL)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/05 14:56

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Results for "( ( plesiochronous&lt;in&gt;metadata ) &lt;and&gt; ( mapping&lt;in&gt;metadata ) )"

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IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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1. **Modeling an E1/TU12 mapper for SDH systems**  
 Silveira, R.; Van Noije, W.A.M.;  
Integrated Circuits and Systems Design, 2000. Proceedings. 13th Symposium on  
 18-24 Sept. 2000 Page(s):171 - 176  
 Digital Object Identifier 10.1109/SBCCI.2000.876026  
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2. **Phase deviations on SDH and SDH/PDH interface**  
 Nowosad, P.; Kuh, G.J.;  
Communications and Signal Processing, 1993., Proceedings of the 1993 IEEE South African  
Symposium on  
 6 Aug. 1993 Page(s):52 - 57  
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3. **Jitter testing technique and results at VC-4 desynchronizer output of SDH equipment**  
 Bregni, S.; D'Agrosa, M.; Valtriani, L.;  
Instrumentation and Measurement Technology Conference, 1994. IMTC/94. Conference  
Proceedings. 10th Anniversary. Advanced Technologies in I & M., 1994 IEEE  
 10-12 May 1994 Page(s):1407 - 1410 vol.3  
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4. **Universal plesiochronous multiplexing (UPM) for arbitrary rate isochronous circuit emulation**  
 Grover, W.D.; Friesen, T.;  
Electronics Letters  
 Volume 32, Issue 19, 12 Sept. 1996 Page(s):1765 - 1766  
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5. **RF modem for high capacity radio system**  
 Hurinville, B.; De Luca, O.;  
Radio Relay Systems, 1993., Fourth European Conference on  
 11-14 Oct 1993 Page(s):244 - 249  
[AbstractPlus](#) | Full Text: [PDF\(348 KB\)](#) IEE CNF

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L2	5702	(plesiochronous or isochronous)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/05 14:37
L12	533	(abstract near5 resource)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/05 14:49
L13	14	2 and 12	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/05 14:49